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Record of Revisions						
Rev.	Date	Sub-Model	Description of change			
1.0	Nov, 24, 2015	-A	Tentative Product Specification was first released.			
2.0	Jan, 22,2016	-A	P26 Add Scan Direction Special Notice			
2.1	Feb,01,2016	-A	P13 update TP_Sync note P17 Modify Power On/Off Sequence			
2.2	Mar,03, 2016	-A	P5&18 Modify VF Spec			
2.3	Mar,30,2016	-A	P6 Add Color chromaticity			
2.4	Aug,3,2016	-A	Modify the Page 13 Electrical characteristics VGH voltage			
L						

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1.0	General description	p.4
2.0	Absolute maximum ratings	p.5
3.0	Optical characteristics	p.6
4.0	Block diagram	p.10
5.0	Interface pin connection	p.13
6.0	Electrical characteristics	p.16
7.0	Reliability test items	p.21
8.0	Outline dimension	p.22
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10.0	Package specification	p.25
11.0	General precaution	p.26

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# 1.0 GENERAL DESCRIPTION

#### 1.1 Introduction

HannStar Display model HSD088IPW1-A is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 8.8 (1:4) inch diagonally measured active display area with (480 horizontal by 1920 vertical pixel) resolution.

#### 1.2 Features

- 8.8 inch (1:4 diagonal) configuration
- 16.7M color
- RoHS/ Halogen Free Compliance

## 1.3 Applications

Automotive

## 1.4 TFT LCD General information

Item		Specification	Unit
Outline Dimension		64.3 x 231.3 (typ)	mm
Display area		54.72(H) x 218.88(V)	mm
Number of Pixel		480 RGB (H) x 1920(V)	pixels
Pixel pitch		0.114(H) x 0.114(V)	mm
Pixel arrangement RGB Vertical stripe			
Display mode Norr		Normally Black	
NTSC		50 (typ.)	%
Surface treatment		НС	
Weight		(100)g (Тур.)	g
Back-light		White LED	
Power Consumption	Logic	0.65 (Max) @ White pattern      Frame rate 60Hz	
	BL System	2.8 (Max) @ Black pattern w/o LED driver	W

## 1.5 Mechanical Information

Item		Min.	Тур.	Max.	Unit
Madula	Horizontal (H)	64.0	64.3	64.6	mm
Module Size	Vertical (V)	231.0	231.3	231.6	mm
	Depth (D)	_	4.8	5.1	mm
Weight	·	_	(100)		g

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# 2.0 ABSOLUTE MAXIMUM RATINGS

# 2.1 Electrical Absolute Rating

# 2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
Power supply voltage	$V_{DD}$	-0.5	4.0	V	
	$V_{GH}$	15	26	V	
	$V_{GL}$	-11.5	-4	V	
	$AV_{DD}$	7	12.5	V	
Logic Signal Input Level	$V_{DD}$	-0.5	4.0	V	

# 2.1.2 Backlight unit

Item	Symbol	Тур.	Max.	Unit	Note
LED current	IL I	160		mA	(1) (2)(3)
LED voltage	VL	16	17.5	V	(1) (2)(3)
LED reverse voltage	V <sub>R</sub>		5	V	
LED forward current	I <sub>F</sub>		80	mA	

Note:

 Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

(2) Ta =25±2℃

(3) Test Condition: LED current 160 mA. The LED lifetime could be decreased if operating IL is larger than 160mA.

# 2.1.3 Environment Absolute Rating

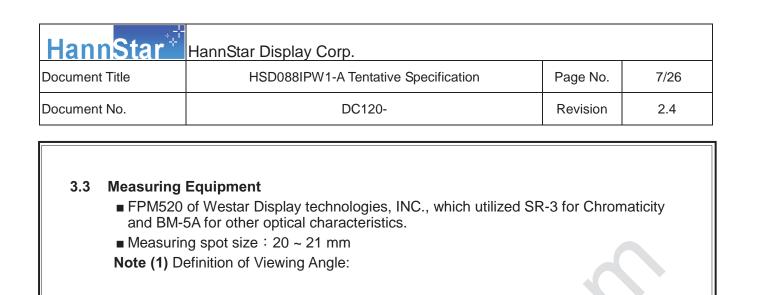
Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T <sub>opa</sub>	-20	70	°C	
Storage Temperature	$T_{stg}$	-30	80	°C	

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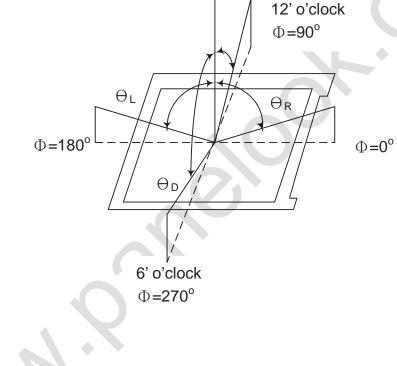
OPTICAL CHARACTERISTICS 3.1 Optical specification								
ltem		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast		CR		600	800	_		(1)(2)(4)
Response time	9	Tr+Tf			30	40	msec	(1)(3)
White luminand (Center)	се	YL		480	600	-	cd/m <sup>2</sup>	(1)(4) (I <sub>L</sub> =160mA)
	<b>D</b>	R <sub>x</sub>	⊖=0	0.576	0.626	0.676		
	Red	R <sub>Y</sub>	Normal	0.295	0.345	0.395		
	Green	G <sub>x</sub>	viewing	0.262	0.312	0.362		
Color		G <sub>Y</sub>	angle	0.493	0.543	0.593		
chromaticity	Blue	B <sub>x</sub>		0.097	0.147	0.197		c-light
(CIE1931)		B <sub>Y</sub>		0.059	0.109	0.159		
	White	W <sub>x</sub>		0.250	0.300	0.350	-	
		Wy		0.272	0.322	0.372		
		$\Theta_{L}$		75	85	_		
	Hor.	θ <sub>R</sub>		75	85	_		
Viewing angle	Mar	θυ	CR>10	75	85	_	]	(1)(4)
	Ver.	θρ		75	85			
Brightness uniformity		B <sub>UNI</sub>	⊖=0 (9point)	70	80	_		(5)
View Direction				Al	_L			(6)

# 3.2 Measuring Condition

- Measuring surrounding : dark room
- LED current I<sub>L</sub>: 160mA
- Ambient temperature : 25±2°C
- 15min. warm-up time.



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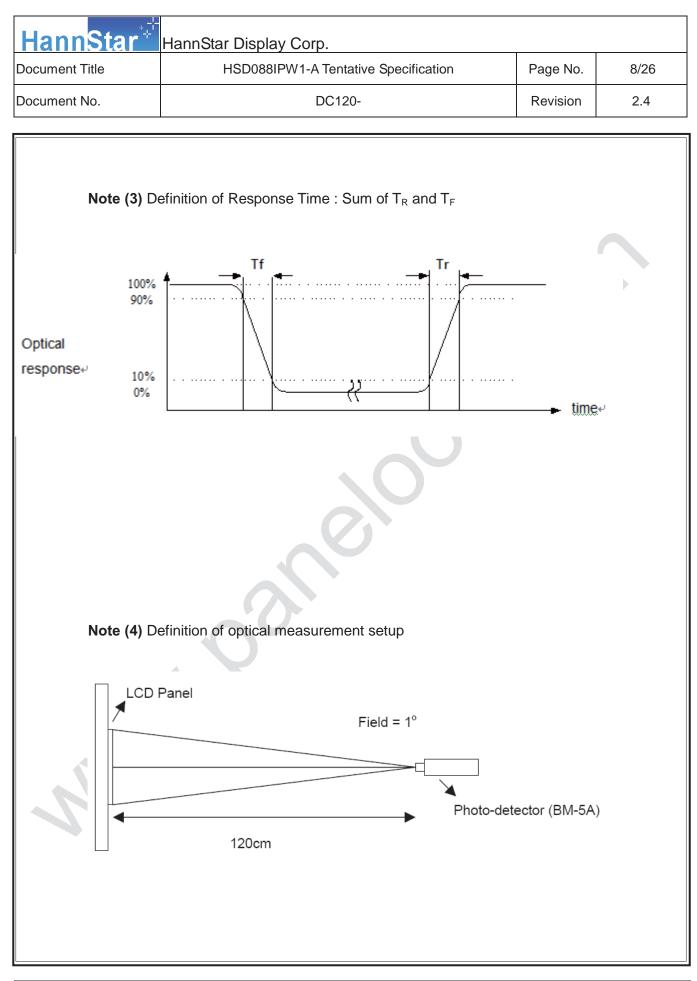


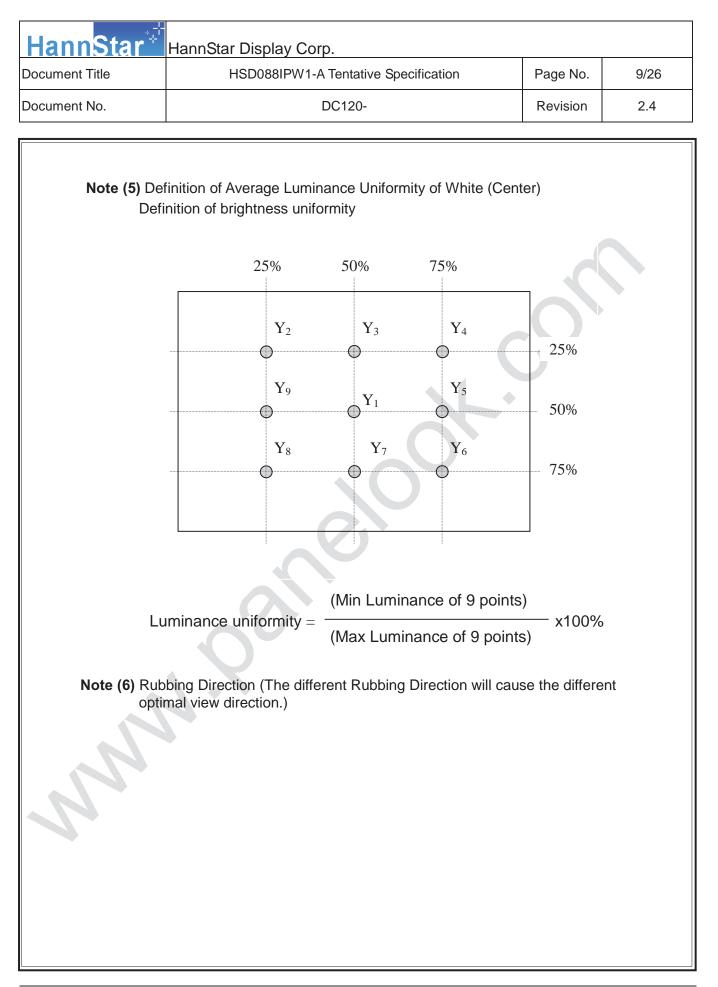
# Note (2) Definition of Contrast Ratio (CR) : measured at the center point of panel

Luminance with all pixels white

CR = ----

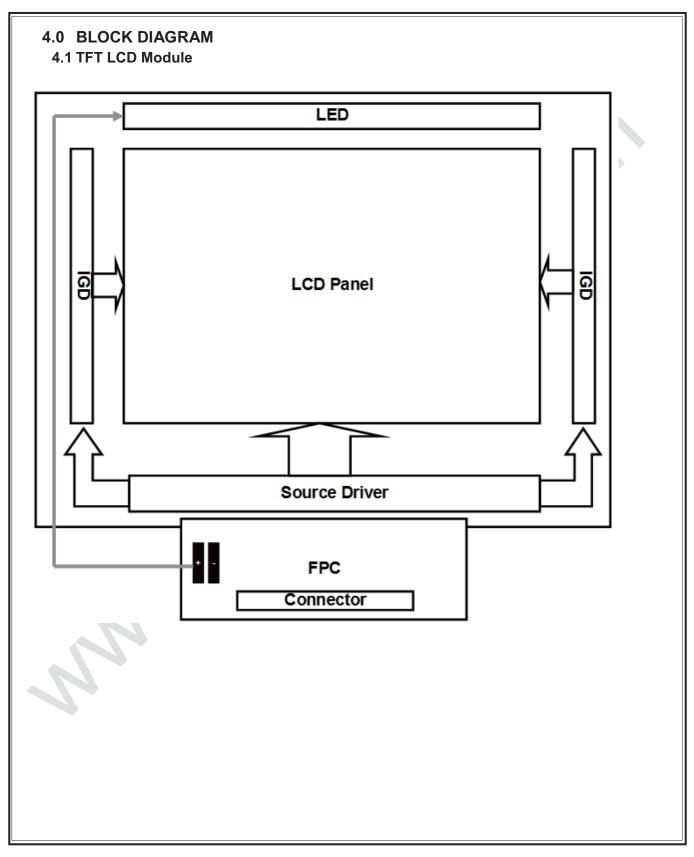
Luminance with all pixels black



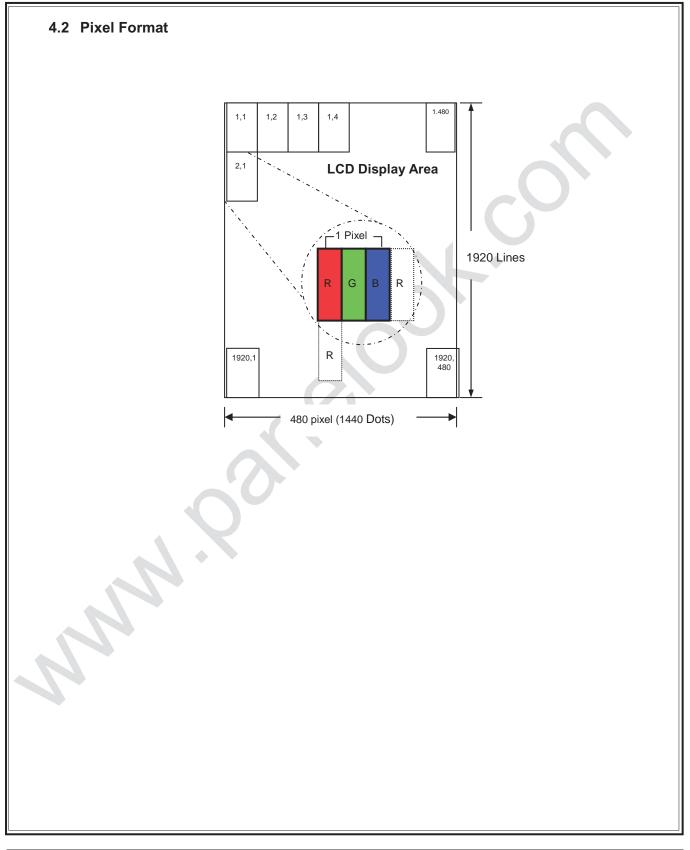


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# 5.0 INTERFACE PIN CONNECTION

FPC connector is used for electronics interface. The recommended model is Hirose FH34SRJ-40S-0.5SH(50)

Pin No.	Symbol	I/O	Function	
1	GND	Ρ	Ground	
2	NC		No connection	
3	LED+	Ρ	LED Anode	
4	LED+	Ρ	LED Anode	
5	NC		No connection	
6	LED-	Ρ	LED Cathode	
7	LED-		LED Cathode	
8	NC		No connection	
9	GND	Ρ	Ground	
10	NC		No connection	
11	AVDD	Ρ	Power supply for analog circuit	
12	NC		No connection	
13	VGH	Ρ	Power supply for analog circuit	
14	NC		No connection	
15	VGL	Ρ	Power supply for analog circuit	
16	NC		No connection	
17	GND	Ρ	Ground	
18	VCOM		Power supply for common voltage	
19	GND		Ground	
20	GND	Ρ	Ground	
21	RESET		Global reset	
22	VDD		Power supply for digital circuits	
23	STBYB		Standby mode	
24			Sync signal for touch panel	
25	GND		Ground	
26	D0P		MIPI Data Input Lane0 positive-end	
27	DON		MIPI Data Input Lane0 negtive-end	
28	GND		Ground	
29	D1P		MIPI Data Input Lane1 positive-end	
30	D1N		MIPI Data Input Lane1 negtive-end	
31	GND	Ρ	Ground	
32	CLKP		MIPI Clock Input positive-end	
33	CLKN		MIPI Clock Input negtive-end	
34	GND		Ground	
35	D2P		MIPI Data Input Lane2 positive-end	
36	D2N		MIPI Data Input Lane2 negtive-end	
37	GND	Ρ	Ground	

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	38	D3P	Ι	MIPI Data Input Lane3 positive-end
Γ	39	D3N	Ι	MIPI Data Input Lane3 negtive-end
Γ	40	GND	Ρ	Ground

## 6.0 ELECTRICAL CHARACTERISTICS

#### 6.1 TFT LCD Module

ltem	Symbol	Min.	Тур.	Max.	Unit	Note			
	VDD	3.0	3.3	3.6	V				
	VGH	19.0	20	21.0	V				
Supply Voltage	VGL	-11	-10	-9	V				
	AVDD	11.8	12	12.2	V				
VCOM	VCOM	4.5	4.88	5.2	V	Note (1)			
Input signal	ViH	0.7 VDD	-	VDD	V	Note (2)			
voltage	ViL	0		0.3 VDD	V	Note (2)			
	IDD	-	-	35	mA	VDD =3.3V			
	IADD	-	-	30	mA	AVDD=12V			
Current of power supply	IGH	-	<u> </u>	5	mA	VGH=18V			
power ouppry	IGL	-	-	-5	mA	VGL= -10V			
	lvcom	-	-	0.1	mA	Vcom= 4.88V			

Note (1): Please adjust VCOM to make the flicker level minimum. Note (2) :RESET 
STBYB 
TP\_Sync

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## 6.2 MIPI DC Characteristics 6.2.1 HS Receiver DC Specification

			Rating			
Parameter	Symbol	Min	Тур	Max	Unit	Note
Operation Voltage	VDD	1.5-10%	1.5	1.5+10%	mV	
Differential Input Voltage	[VID]	70	200	260	mV	
Common Mode Voltage	V <sub>CMRX(DC)</sub>	70	-	330	mV	
Differential Input High Threshold Voltage	VTH	-	-	70	mν	$\mathbf{O}$
Differential Input Low Threshold Voltage	VTL	-70	-	-	mV	
Singled-ended input high voltage	VIHHS	-	-	460	mV	
Singled-ended input low voltage	VILHS	-40	-		mV	
Singled-ended threshold for HS termination enable	$V_{TERM-EN}$	-	-	450	mV	$\langle \rangle$
Differential input impedance	Z <sub>ID</sub>	80	100	125	ohm	
Pin leakage current	ILEAK	-10		10	uA	
Common-mode interference beyond 450MHz	$\Delta V_{\text{CMRX(HF)}}$	-	0	100	mV	
Common-mode interference 50MHz - 450MHz	$\Delta V_{\text{CMRX(LF)}}$	-50	) - 、	50	mV	
Common-mode termination	Ссм	-	-	60	pF	
Embedded Termination	RT	90	100	110	ohm	2bits RT_SEL[1: 0] for termination resistor selection $00 \rightarrow 2000hm$ $10, 01 \rightarrow 1500hm$ $11 \rightarrow 1000hm$ (default) 1bit ERMR_EN for termination resistor enable TERMR_EN=0, termr disable R=(OPEN) TERMR_EN=1, termr enable

Note:

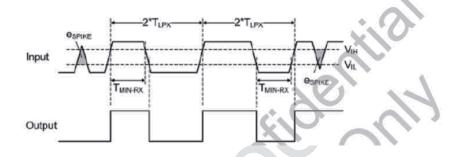
(1) Excluding possible additional RF interference of 100mV peak sine wave beyond 450MHz.

(2) This table value includes a ground difference of 50mV between the transmitter and the receiver, the static common-mode level tolerance and variations below 450MHz.

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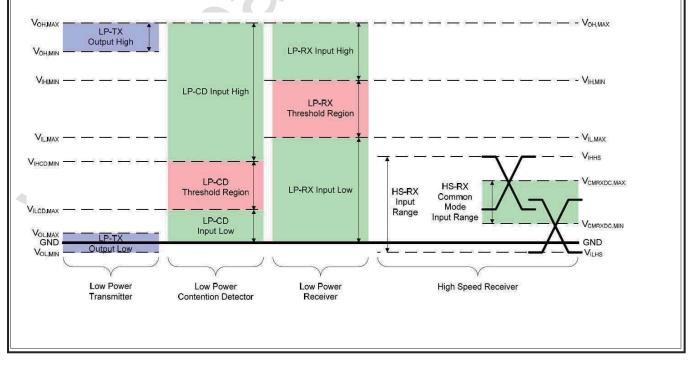
## 6.2.2 LP Receiver DC Specification

			Rating			
			Rating			
Parameter	Symbol	Min	Тур	Мах	Unit	Note
Logic 1 input voltage	ViH	880	a	*	mV	
Logic 0 input voltage, not in ULP State	V <sub>IL</sub>	, e ,	Ξ.	550	mV	25. 251
Input hysteresis	V <sub>HYST</sub>	25	8	9	mV	



## 6.2.3 Line Contention Detection

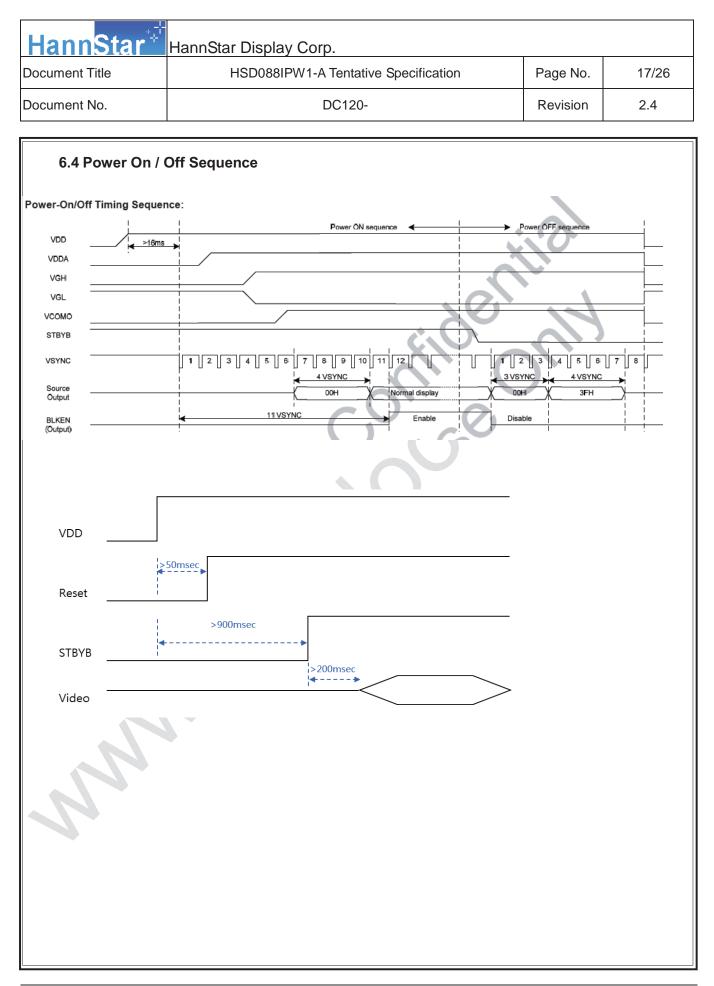
Parameter	Symbol	Min	Rating Typ	Max	Unit	Note
Logic 1 contention threshold	VIHCD	450	-	-	mV	
Logic 0 contention threshold	VILCD			200	mV	



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## 6.3 Interface Timing

Item	Symbol	Min.	Тур.	Max.	Unit
MIPI Video data rate(4 lane)	-	-	397.7	-	Mbps
PCLK Frequency	FPCLK	-	66.3	-	MHz
Horizontal Synchronization	Hsync	-	30	-	PCLK
Horizontal Back Porch	HBP	-	30		PCLK
Horizontal Front Porch	HFP	-	30	-	PCLK
Hsync+HBP+HFP	-	75	90	-	PCLK
HorizontalAddress(Display Area)	Hadr	-	480	-	PCLK
Horizontal cycle	-	555	570	-	PCLK
Vertical Synchronization	Vsync		6	-	Line
Vertical Back Porch	VBP	$\mathcal{O}$	6	-	Line
Vertical Front Porch	VFP	-	6	-	Line
Vsync+VBP+VFP	<u>}-</u>	15	18	-	Line
Vertical Address(Display Area)	Vadr	-	1920	-	Line
Vertical cycle	-	1935	1938	-	Line
Frame Rate	-	-	60	-	Hz



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Parameter	Symbol	Min	Тур	Мах	Units	Condition
LED Current	I <sub>F</sub>		160		mA	<b>Ta=25</b> ℃
LED Voltage	V <sub>F</sub>			17.5	Volt	<b>Ta=25</b> ℃
LED Life-Time	N/A		30,000		Hour	Ta=25℃ I <sub>F</sub> =160mA Note (2)

Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition:  $Ta=25\pm3$  °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=160mA. The LED lifetime could be decreased if operating IL is larger than 160mA. The constant current driving method is suggested.

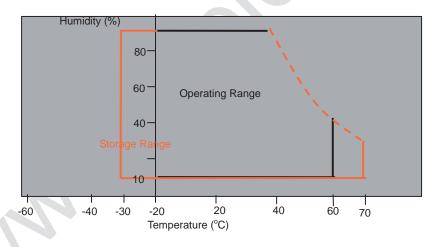
Note (3) LED Light Bar Circuit

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No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+80°C, 240hrs	
2	Low Temperature Storage	Ta=-30°C, 240hrs	
3	High Temperature Operation	Ta=+70°C, 240hrs	
4	Low Temperature Operation	Ta=-20°C, 240hrs	
5	High Temperature and High Humidity (operation)	Ta=+60°C, 90%RH, 240hrs	<u>}</u>
6	Thermal Cycling Test (non operation)	-20°C(30min)→+70°C(30min),100 cycles	
7	Vibration	Sine Wave 1.5G, 5~500Hz, XYZ 30min/each direction	
8	Shock	Half-Sine, 200G, 2ms, ±XYZ, 1time	

Note: There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

## Storage / Operating temperature

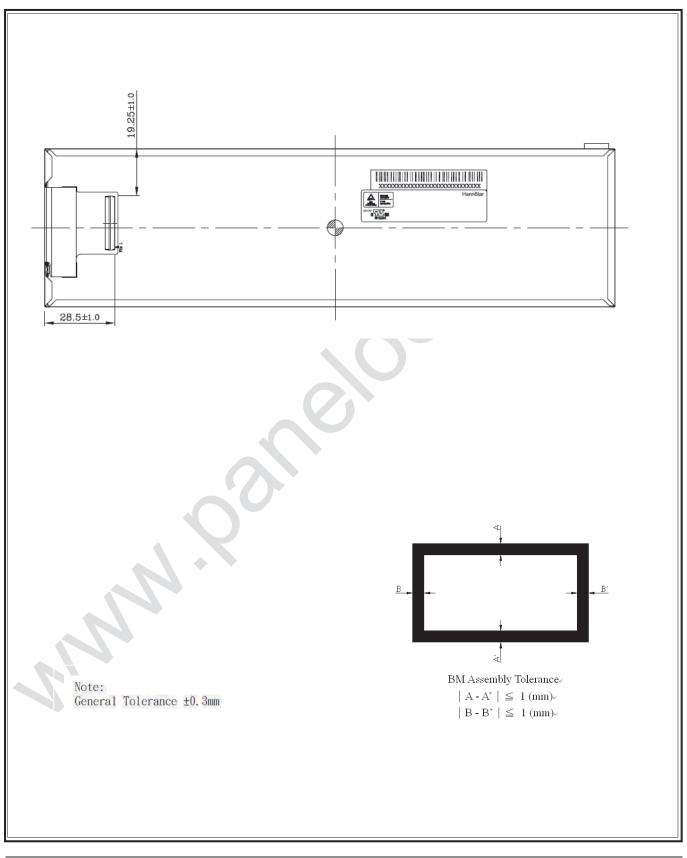


Note .Max wet bulb temp.=39°C

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: mm
4.8±0.3
±0.3 ector)

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## 9 LOT MARK

## 9.1 Lot Mark



Code 1,2,3,4,5,6: HannStar internal flow control code.

Code 7: production location.

Code 8: production year.

Code 9: production month.

Code 10,11,12,13,14,15: serial number.

#### Note (1) Production Year

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Mark	6	7	8	9	0	1	2	3	4	5

## Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	А	В	С

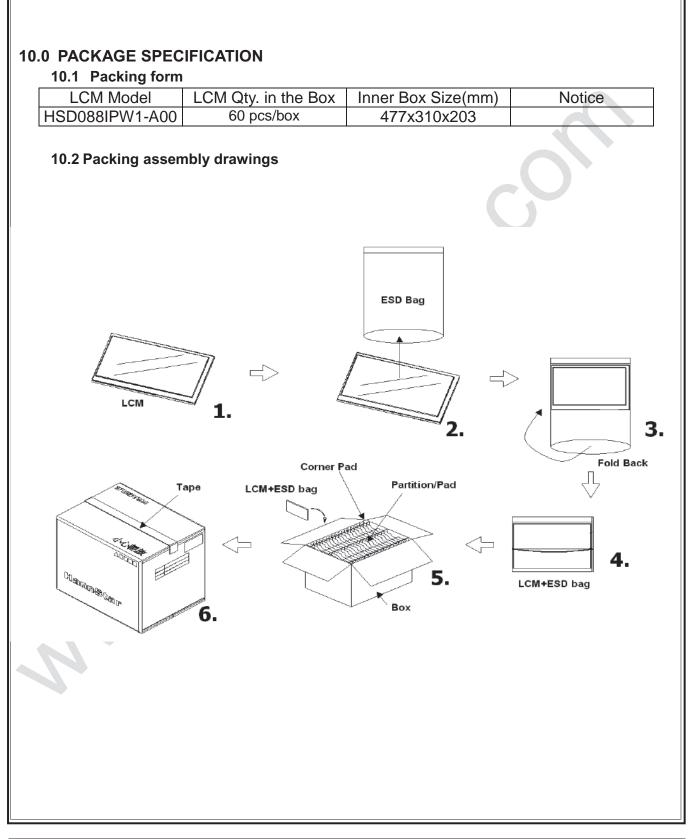
## 9.2 Location of Lot Mark

- (1) Location: The lot mark is attched to the back side of the LCD module. See Product back view. ( Section 8.0 : OUTLINE DIMENSION )
- (2) Detail of the Lot mark: Print 15 code as lot mark (see 9.1 Lot Mark)
- (3) This is subject to change without prior notice.



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## 11.0 GENERAL PRECAUTION

#### 11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

## 11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

## 11.3 Breakage of LCD Panel

- 11.3.1.If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 11.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 11.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 11.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

## 11.4 Electric Shock

- 11.4.1. Disconnect power supply before handling LCD module.
- 11.4.2. Do not pull or fold the LED cable.
- 11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

## 11.5 Absolute Maximum Ratings and Power Protection Circuit

- 11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 11.5.3. It's recommended to employ protection circuit for power supply.

## 11.6 Operation

- 11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

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- 11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

#### 11.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

#### 11.8 Static Electricity

- 11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 11.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

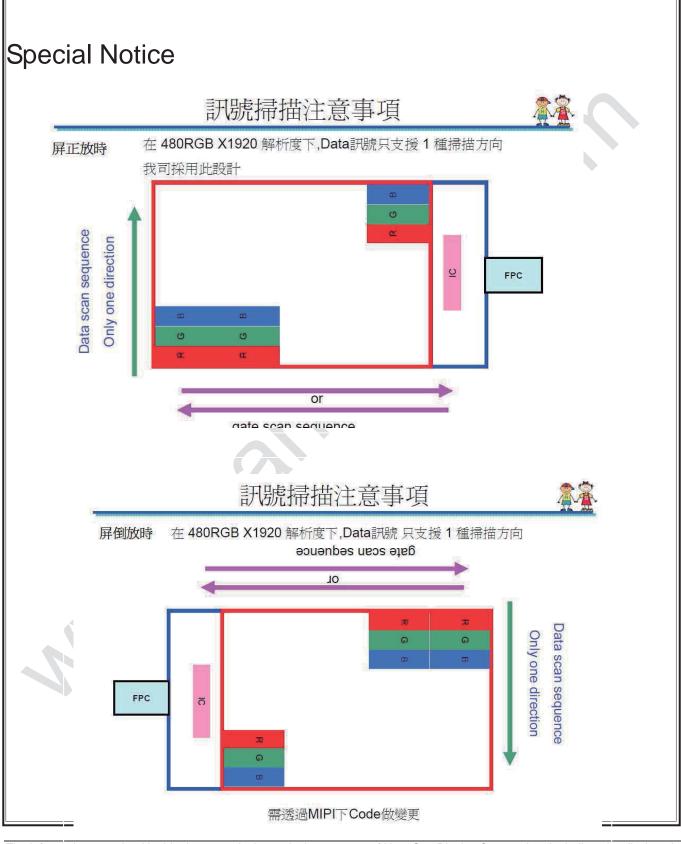
#### 11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

#### 11.10 Disposal

When disposing LCD module, obey the local environmental regulations.

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